

The Silicon Vertex Detector of the Belle II Experiment

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36 The Belle II experiment operating at the asymmetric-energy e^+e^- SuperKEKB collider, located in Tsukuba (Japan), has been collecting data since March 2019. Its excellent vertexing abilities are provided by the vertex detector, part of which is the silicon-strip vertex detector (SVD) that plays a crucial role in the charged-particle tracking close to the interaction point. The SVD has operated successfully and efficiently over the whole period of data taking so far. In this article, we briefly discuss its purpose, structure and basic description of the front-end electronics. The main quantities related to the SVD performance are presented. The foreseen increase in SuperKEKB luminosity will lead to higher background, so we describe its impact on the SVD performance. A quick overview of the radiation damage campaign is presented to show the predicted behaviour of the sensors subjected to high radiation, whose level is constantly monitored. We also discuss the ongoing software development to account for the high occupancy expected in the future. In particular, the utilization of the SVD hit time information is presented as a very important quantity to suppress off-time background hits and tracks. Finally, the work done during the first long shutdown of SuperKEKB is briefly described, during which a major upgrade of the pixel detector has been successfully done. Resumption of the beam operation is expected in early 2024.

Keywords: Silicon strip detector, Vertex detector, Tracking detector, Belle II

37 1. Introduction

38 The Belle II [1] experiment is dedicated to search for physics beyond the standard model at
 39 the intensity frontier. It operates at the SuperKEKB collider located at KEK, Tsukuba in Japan,
 40 providing asymmetric beams of 7 GeV electrons and 4 GeV positrons. In the accelerator's default
 41 operation regime, the center-of-mass energy is set to the $\Upsilon(4S)$ resonance, hence it produces a huge
 42 sample of B mesons via the $e^+e^- \rightarrow \Upsilon(4S) \rightarrow B\bar{B}$ process. So far, SuperKEKB achieved the
 43 highest instantaneous luminosity of $4.7 \times 10^{34} \text{ cm}^{-2}\text{s}^{-1}$, which is the current world record. The
 44 Belle II detector is a multipurpose spectrometer characterized by excellent vertexing capability and
 45 good hermeticity, which has accumulated 424 fb^{-1} to date, and its final goal is to collect a data
 46 sample of 50 ab^{-1} , that will be possible with a constant increase of the SuperKEKB instantaneous
 47 luminosity up to our final goal of $6 \times 10^{35} \text{ cm}^{-2}\text{s}^{-1}$.

48 Belle II is composed of various sub-detectors with the vertex detector (VXD) being the closest
 49 to the interaction point. It is divided into two further subsystems. The innermost part is the pixel
 50 detector (PXD), which is based on depleted field effect transistor pixel sensors. The PXD consists
 51 of two layers (numbered 1-2) and its main goal is the precise determination of the decay vertices.
 52 Outside the PXD is the silicon-strip vertex detector (SVD) [2] with four layers (numbered 3-6)
 53 that mostly extrapolates the measured tracks to the PXD, defining the so-called region of interest
 54 (ROI), which significantly reduces the amount of data recorded by the PXD. The SVD also performs
 55 standalone tracking for low-momentum charged particles and contributes to their identification by
 56 providing energy loss information.

57 2. SVD structure

58 Each SVD layer is composed of a number of double-sided silicon strip detectors (DSSDs)
 59 that are manufactured on an n-type bulk wafer with a thickness of about $300 \mu\text{m}$ (Figure 1). One
 60 side of the sensor is covered by the p-type silicon strips placed in parallel to the beam axis that
 61 determine the $r - \phi$ coordinates (distance from the z -axis and azimuthal angle, respectively), and
 62 the n-type strips are placed perpendicularly on the other side of the bulk, measuring the z coordinate
 63 (collinear to the electron beam). Figure 1 (left) shows a schematic picture of SVD layers and
 64 associated sensors with increasing numbering from the forward (FWD) to the backward (BWD)
 65 regions. Such structure is repeated along the azimuthal direction forming different ladders and the
 66 so-called windmill geometry of the SVD. The sensors differ depending on the layer and the region
 67 in which they are placed in the SVD. In the FWD part, for layers 4-6, they have a trapezoidal shape

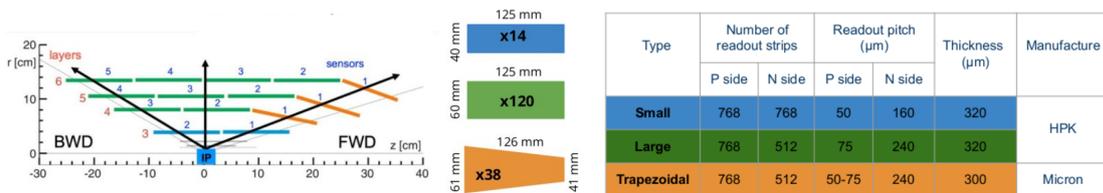


Figure 1: Schematic picture of SVD sensors forming different layers (left) and a table summarizing the parameters for each type of sensor (right).

68 and are slanted in the region that, due to the asymmetric beams, is characterised by the highest track
69 multiplicity. In addition, in layer 3 the sensors are smaller and contain more n-type strips than that
70 in layers 4-6. This also implies the readout pitch (distance between two readout strips) to be much
71 smaller for p-side strips with respect to the n-side. To improve spatial resolution, a floating strip is
72 placed between two readout strips on both p- and n-sides. The charge induced in the floating strip
73 is shared by the neighboring strips, reducing the effective strip pitch to half of the readout pitch.
74 The right table of Figure 1 summarises the sensor parameters. The SVD consists of 224 thousand
75 readout strips and 172 sensors with an active area of 1.2 m².

76 2.1 Front-end electronics

77 For the readout we use APV25 chips [3]. For the central part of SVD (except for layer 3), the
78 chips are attached directly to the DSSD sensors via flex circuits bent over the DSSD edge (Origami
79 concept). The edge sensors use hybrid boards located outside the active volume. The APV25 has
80 128 channels per chip and amplifiers that provide a shaping time of 50 ns. Radiation hardness
81 exceeds 100 Mrad and the power consumption is around 0.4 W/chip. The sampling frequency is 32
82 MHz and after the trigger's arrival we can collect six consecutive signal samples in total with the
83 multipeak mode. To account for higher luminosity in the future, we have introduced the so-called
84 "3/6 mixed acquisition mode", which allows switching between three and six samples recorded on
85 an event-by-event basis, based on the trigger type (and hence its time accuracy) for a particular
86 event. This mode, already prepared and tested, significantly reduces the data size, which can be
87 crucial in high background conditions.

88 3. SVD performance

89 Since the start of the operation we have observed very smooth performance of the SVD, with a
90 very few masked strips (less than 1%). Moreover, the environment has been stable and the evolution
91 of calibration constants is consistent with expectation. Also, the effects of radiation damage are
92 well under control.

93 Several quantities related to the SVD performance - sensor efficiency, signal-to-noise ratio, and
94 both spatial and time resolution - are constantly monitored. Regarding SVD sensor efficiency, the
95 values for all sensors are typically over 99% and they are also very stable over the whole period of
96 data taking. Clusters are formed from adjacent strips with significant signal and the charge collected
97 in a given cluster strongly depends on the incident angle of the track. Over time, we observe very
98 similar cluster charge in all the sensors once normalized to the track's length. For layer 4-5-6 on
99 the n-type strips we observe 10-30% loss of the signal due to the large pitch combined with the
100 presence of a floating strip. Another important quantity is the signal-to-noise ratio (SNR), which is
101 satisfactory for all 172 sensors. The SNR MPV is ranging from 13 to 30, depending on the sensor
102 position, due to the track incident angle with the sensor, and on the sensor side, with smaller SNR
103 for the p-sides, due to larger noise for the longer strip length. A small decrease of cluster SNR value
104 is observed in 2022 measurement, due to increased noise from radiation damage by approximately
105 20%-30%. In Figure 2 the distributions of cluster charge (left) and SNR (right) are presented,
106 where histograms representing the data collected in 2020 and 2022 are superimposed.

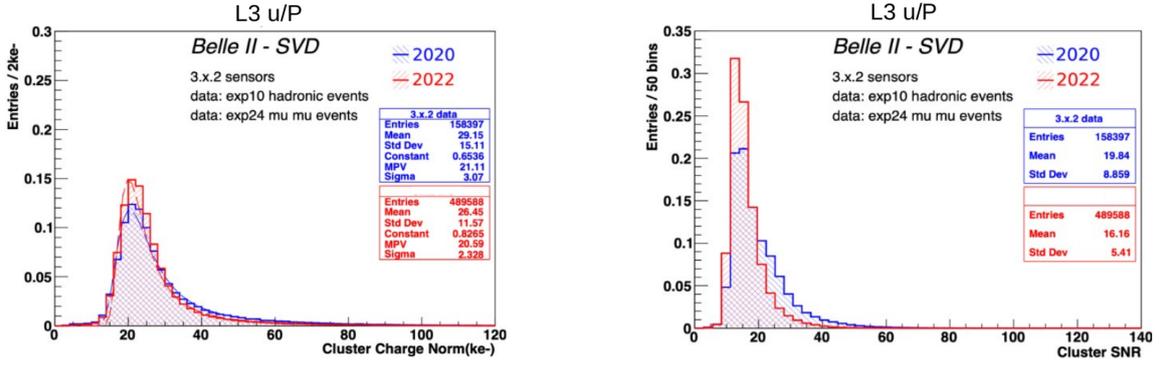


Figure 2: Distribution of cluster charge (left) and signal-to-noise ratio (right) for layer 3 (p-side). Comparison between data taken in 2020 (blue) and 2022 (red) is presented.

107 Both position and time resolution are very important metrics for excellent SVD performance.
 108 The position resolution measurement is based on the residuals, i.e., the clusters' positions with
 109 respect to the intercept of the unbiased tracks' extrapolation, and it is evaluated with a large sample
 110 of $e^+e^- \rightarrow \mu^+\mu^-$ decays. As shown in Figure 3, this quantity depends on the incident angle and is
 111 very stable during the period of the Belle II operation. As seen in Figure 3, the resolution for the
 112 n-side (left plot) is about two times worse with respect to that for the p-side, which is a result of the
 113 different pitch. **As the calculation of the cluster position is based on the collection of the charge,**
 114 **which is shared among the strips, the worse resolution near zero-degree incident angle results from**
 115 **the increased likelihood of a single-strip cluster. Also, a small deterioration of the resolution at large**
 116 **angles is observed as a consequence of an increased multiple scattering probability of a passing**
 117 **particle.**

118 Hit time resolution is measured with respect to the event time of the collision provided by
 119 central drift chamber (CDC) and exhibits a very good resolution of less than 3 ns for the clusters
 120 associated to tracks. Using the average value of all the hits on a given track, the so-called "track-
 121 time" can be computed, slightly improving the time resolution. Furthermore, the "event-time"
 122 can be determined using all the clusters associated to selected tracks in an event. **In such a way,**
 123 **the "event time" can be computed by the SVD with a resolution of the order of 1 ns, while the**
 124 **computation is around 2000 times faster than the one based on CDC.** This feature will be especially
 125 important in the higher luminosity environment, as it can significantly speed up the reconstruction
 126 process at the high-level trigger.

127 4. Radiation effects

128 In the high-energy physics experiments, the effects from radiation damage coming from ma-
 129 chine related background is a major factor that **degrades** the sensor performance with time. The
 130 SVD accumulated dose is constantly measured using data from diamond sensors that are mounted
 131 on the IP beam pipe, and the corresponding level of the equivalent neutron fluence is evaluated using
 132 the ratio of equivalent neutron fluence to dose estimated from Monte Carlo simulation. Several
 133 effects related to radiation damage must be taken into account. A linear increase of the leakage
 134 current as a function of radiation damage is observed in the sensors, as expected from the bulk

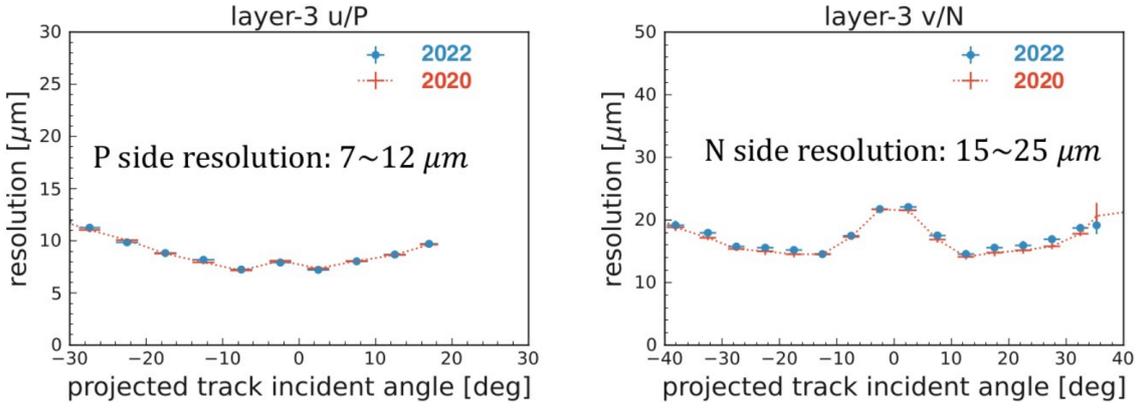


Figure 3: Distributions of position resolution for *p*-side (left) and *n*-side (right) as a function of the track incident angle. A comparison between data taken in 2020 (dots) and 2022 (dotted lines) is presented.

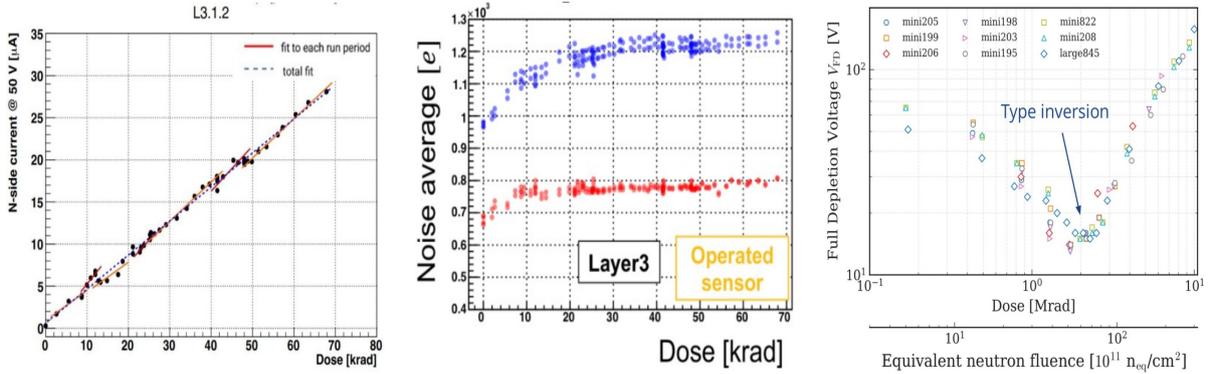


Figure 4: Left plot: Leakage current as a function of the accumulated dose; Center plot: the average noise level as a function of the accumulated dose for the *p*-side (blue dots) and *n*-side (red dots); Right plot: full depletion voltage as a function of the accumulated dose with the type inversion observed at 2 Mrad.

135 damage described by the NIEL model [4], and shown in Figure 4 left. The sensor current is shown
 136 as a function of the accumulated dose for one of the layer 3 sensors most exposed, that received
 137 about 70 krad to date, corresponding to an equivalent neutron fluence of about $1.6 \times 10^{11} \text{ n}_{\text{eq}}/\text{cm}^2$.
 138 So far, this increase has had a negligible contribution to the noise because of both the small leakage
 139 current and the short APV25 shaping time. The rate of the leakage current increase measured
 140 is consistent with the experience from other experiments working with similar detectors and in
 141 comparable conditions [5]. However, we expect some significant impact on the strip noise due to
 142 the sensor leakage current, and hence a deterioration in SNR, for the dose of ~ 6 Mrad, which is
 143 considered as SVD dose limit to preserve optimal performance. The strip noise for unirradiated
 144 modules is dominated by the interstrip capacitance. During the operation we have observed an
 145 increase in its value of about 20% (30%) for *n*-side (*p*-side), due to effects of surface radiation
 146 damage that increases the interstrip capacitance, but it is expected to saturate, as also visible in
 147 Figure 4 center.

148 Another relevant effect of the bulk radiation damage is the impact on depletion voltage.
149 The expected future radiation levels at the nominal luminosity, of about 0.35 Mrad/year and $8 \times$
150 10^{11} $n_{eq}/cm^2/year$, are affected by large uncertainty due to the machine evolution as well as a
151 possible redesign of the interaction region. To better explore the possible effects of bulk damage in
152 the SVD sensors after bulk type inversion, an irradiation campaign was conducted in July 2022 at
153 ELPH, Tohoku University. Several SVD sensors have been exposed to a 90 MeV electrons beam,
154 up to 10 Mrad, corresponding to an equivalent neutron fluence of 3×10^{13} n_{eq}/cm^2 . The decrease
155 of the depletion voltage has been observed up to the point of bulk type inversion, which occurred at
156 2 Mrad ($\sim 6 \times 10^{12}$ n_{eq}/cm^2), after which the depletion voltage started to increase again (Figure 4
157 right). Detailed measurements, whose results will be shortly published, confirmed that the sensors
158 will still work fine after the type inversion, which meets our expectation for these types of silicon
159 detectors. Since the beginning of the detector operation, we have not observed any change in the
160 depletion voltage in the sensors installed in the SVD, as expected due to the small accumulated
161 equivalent neutron fluence so far, below 2×10^{11} n_{eq}/cm^2 . Considering all these results, the dose
162 limit of 6 Mrad and the extrapolation of the background levels quoted above, the SVD has a wide
163 safety margin for the accumulated radiation damage even after 10 years of the operation at the target
164 luminosity.

165 5. High background scenario and related software/hardware developments

166 With the increase of the luminosity and the expected larger machine related background, the
167 SVD occupancy will also increase and a deterioration of the tracking performance is expected above
168 certain levels. So far, the average hit occupancy is 0.5% for layer 3, which does not degrade the
169 performance. Nonetheless, the background extrapolation for different future scenarios has been
170 performed with detailed simulations of the various contributions (beam-gas, Toushek, etc.) and
171 applying appropriate data-simulation scale factors [6]. These studies predict that for the nominal
172 luminosity we can reach an occupancy in layer 3 very close to the limit of 4.7%, above which the
173 tracking performance deteriorates. These predictions have large uncertainties coming from poorly
174 known machine evolution in the future, with a possible redesign of the interaction region. In the
175 most conservative scenario, the layer-3 occupancy can increase up to $\sim 8.7\%$, which is far beyond
176 the modest tracking performance. Such a scenario motivates us to develop the SVD reconstruction
177 software, as well as to seriously consider the VXD upgrade [7], since the safety factor might be too
178 small to ensure good quality data. The technology assessment related to this hardware upgrade is
179 currently ongoing.

180 An important effort related to the software development is the utilization of the hit time
181 information from the SVD. The real signal hits come from well-triggered collisions, but the SVD
182 acquisition window (~ 100 ns) is much wider with respect to the SuperKEKB bunch spacing (6 ns).
183 Therefore, we need to cope with many off-time hits related to the beam-induced background or
184 background from the other bunches. The current selection is based on two requirements: a) time
185 difference between p- and n-side cluster, $|t_p - t_n| < 20$ ns, and b) the absolute value of the cluster
186 time, $|t_{p,n}| < 50$ ns. These criteria reject the majority of the background hits retaining above 99% of
187 the signal, and based on them the SVD occupancy limit for layer 3 can be set at 4.7%. Recently, a
188 more effective background suppression method has been developed in the form of so-called “SVD

189 grouping”. It is based on an event-by-event classification of the clusters by their time, so the
190 clusters belonging to tracks from the same collisions are collected in the same group. Clusters
191 from the different collisions or beam background will be placed in the other groups; finally, only
192 the clusters belonging to the priority group will be used for the tracking. This feature reduces the
193 fake rate (fraction of the fake tracks) by 16% for the high-background scenario. An additional fake
194 rate reduction can be achieved by applying the selection on the track-time to reject off-time tracks.
195 Finally, these improvements allow an increase of the SVD occupancy limit for layer 3 from 4.7% to
196 around 6%.

197 6. Activities during the Long Shutdown 1

198 Long shutdown 1 (LS1) started in May 2022 and one of the goals was to upgrade the VXD with a
199 new PXD. During the first data taking period, the second layer of PXD was only partially equipped,
200 and 5/6 of the azimuthal angle remained uncovered. The new PXD provides the full coverage,
201 which is beneficial for more precise vertexing. Hardware activities for the VXD uninstallation and
202 reinstallation were intense: after the VXD extraction from Belle II, the SVD was detached from
203 the old PXD (May 16-17, 2023), then the new PXD was attached to the SVD (June 20-21, 2023)
204 and finally the complete VXD was installed in the Belle II detector. The whole delicate procedure
205 had neither major problems nor caused any damage. In the period of September 12 - October 1,
206 2023, the VXD commissioning was performed to confirm the PXD and SVD performance, and
207 also to check the impact from the increased PXD power consumption and possible increase in
208 the temperature on the sensor leakage current. From September 21, several cosmic runs with no
209 magnetic field were taken to check the performance and compare them with corresponding ones
210 for 2022 data samples. We observed no issues, in particular the noise distributions over readout
211 channels remained basically unchanged as well as SNR for the clusters associated to the tracks, with
212 stable excellent efficiency for all the sensors.

213 7. Conclusions

214 To conclude, SVD has successfully operated since March 2019 with very smooth performance
215 and without major problems. Its good vertexing quality has been confirmed by many physics
216 measurements, in particular those related to the lifetime analyses e.g. Ref. [8]. Some radiation
217 damage effects were observed, but without any impact on the performance so far.

218 However, the extrapolated background level indicates that the occupancy in the SVD can exceed
219 the current limit that **guarantees** good tracking performance. Hence, several software improvements
220 are being implemented to account for high background conditions. In particular, exploitation of
221 the SVD hit time is of major importance. Alongside, a VXD upgrade is also under discussion to
222 increase robustness against high background and to match a possible new interaction region.

223 The VXD reinstallation at Belle II with complete PXD has been successfully done during the
224 LS1, followed by successful VXD commissioning with cosmic data. The beam operation is planned
225 to resume in early 2024.

226 **References**

227 [1] T. Abe et al., Belle II Technical Design Report, arXiv:1011.0352 (2010).

228 [2] K. Adamczyk et al., JINST **17**, P11042 (2022).229 [3] M. J. French et al., Nucl. Instrum. Meth. A **466**, 359 (2001).230 [4] G. Lindstrom et al., Nucl. Instrum. Meth. A **465**, 60-69 (2000).231 [5] B. Aubert et al., Nucl. Instrum. Meth. A **729**, 615 (2013).232 [6] A. Natochii et al., Nucl. Instrum. Meth. A **1055**, 168550 (2023).233 [7] M. Babeluk et. al., Nucl. Instrum. Meth. A **1048**, 168015 (2023).234 [8] F. Abudinén et al., Phys. Rev. Lett. **130**, 071802 (2023)